

PATENT ABSTRACTS OF JAPAN

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(71)Applicant : MATSUSHITA ELECTRIC WORKS LTD

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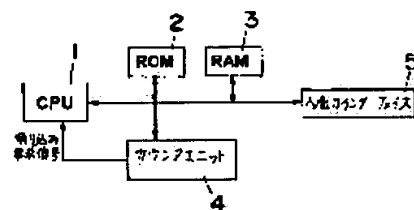
(72)Inventor : NAKAI MASARU

(54) PROGRAMMABLE CONTROLLER

(57)Abstract:

PURPOSE: To obtain a programmable controller which can convert easily a constant of an input filter means.

CONSTITUTION: A counter unit 4 counts an external clock and compares a counted value with a target value and when the values coincide with each other, an interruption request signal is applied to a CPU 1. When the interruption request signal is inputted, the CPU 1 reads out the data of an external input signal and executes a sampling processing and in the case its data coincides with the previous data, it is adopted as the data of the present external input signal and stored in an input image memory constituted of a RAM 3. When a filter constant changing instruction is received, the CPU 1 fetches filter the constant sent together with the instruction, sets the fetched filter constant as a target value and changes the target value of the counter unit 4.



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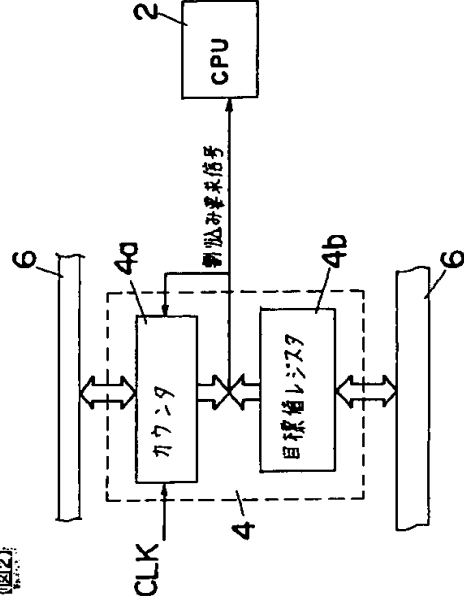
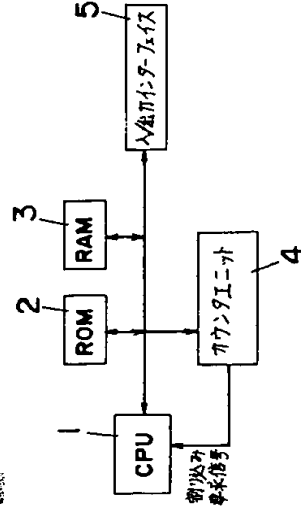
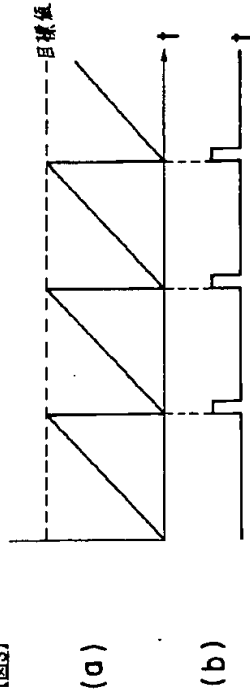
リング記憶用メモリ[a]、サンプリングタイミング計算用メモリ[b]を構成する。

【図6】一方、CPU1はシステムの基本クロックを用いてサンプリング計算と、上記メモリ[a]のデータ0の送受、更新の処理を行なう手段を動作プログラムにより構成する。尚プログラムコンポントラウとして他の構成は図1から図5を参照する。

【0017】而して本実施例ではCPU1により図6に示すように■でサンプリングタイミング計算用メモリ[a]にセットした一

[illegible][illegible][illegible]

[0019]

[illegible]

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図4

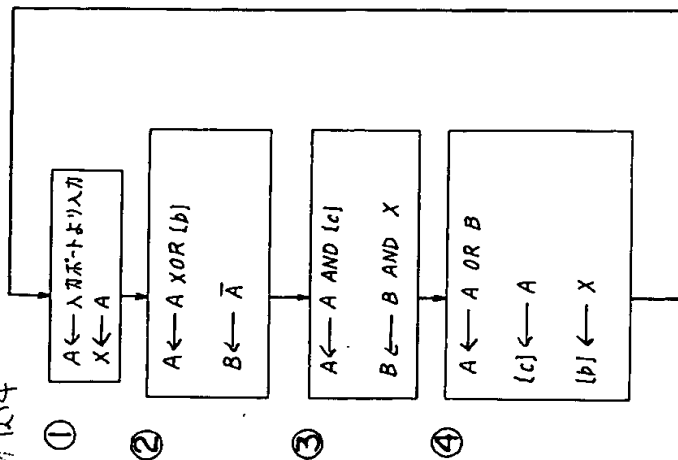
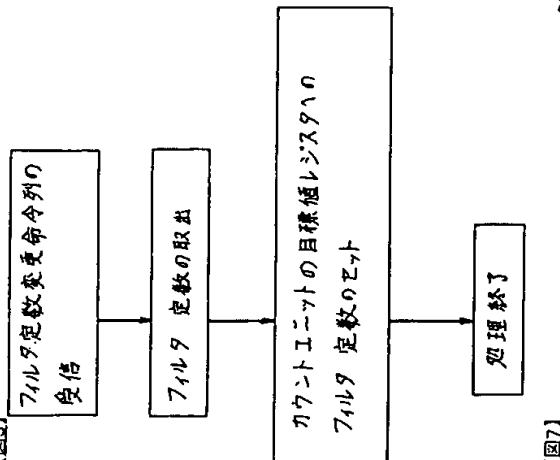


図5



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図6

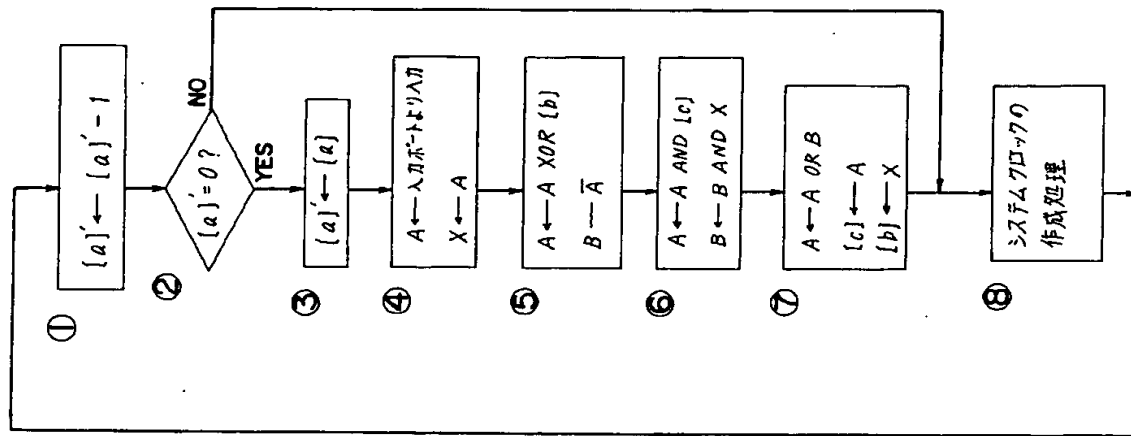
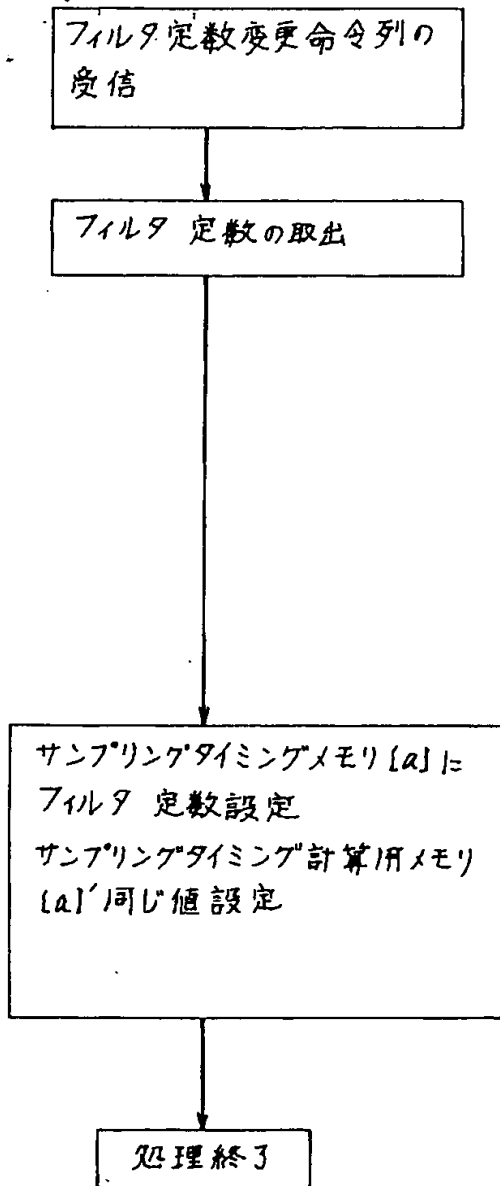


図7



【図6】

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